UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.   : 7,346,755 B2

APPLICATION NO. : 10/663128

DATED   : March 18, 2008

INVENTOR(S)   : Ken Gary Pomaranski et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (57), under "Abstract", lines 1-15, delete "An example memory quality assuring system is provided. The system may include a memory mapping logic configured to facilitate accessing memory locations and redirecting memory accessing operations. The system may also include a memory quality assurance logic configured to logically replace a first memory location with a second memory location, to initiate testing logically isolated memory locations, and to selectively logically remove tested memory locations based on the testing.

It is emphasized that this abstract is provided to comply with the rules requiring an abstract that will allow a searcher or other reader to quickly ascertain the subject matter of the application. It is submitted with the understanding that it will not be employed to interpret or limit the scope or meaning of the claims 37 CFR 1.72(b)." and insert -- Methodology, systems, and media associated with memory quality assurance are described. One exemplary method embodiment comprises selectively copying the contents of a first memory location to a second memory location, logically replacing the first memory location with the second memory location and initiating memory testing of the first memory location without an operating system interaction. --, therefor. (Preliminary Amendment to abstract filed on Oct. 02, 2003, page 1, lines 11-15)

In column 16, line 56, delete "A. Gamer, A Dictionary of Modem" and insert -- A. Garner, A Dictionary of Modern --, therefor.

In column 17, line 29, in Claim 5, delete "first memory" and insert -- memory --, therefor.

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,346,755 B2
APPLICATION NO. : 10/663128
DATED : March 18, 2008
INVENTOR(S) : Ken Gary Pomaranski et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

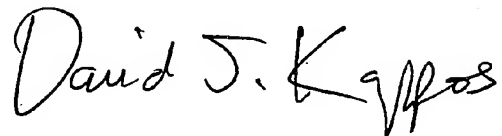In column 17, line 43, in Claim 7, delete "pad," and insert -- part, --, therefor.

In column 17, line 53, in Claim 9, delete "loge," and insert -- logic, --, therefor.

In column 18, line 67, in Claim 27, after "zero test" insert -- , --.

In column 19, line 28, in Claim 29, after "of memory" delete "." and insert -- , --, therefor.

Signed and Sealed this

Seventeenth Day of November, 2009

David J. Kappos
Director of the United States Patent and Trademark Office